

Integrated Phase and Frequency Synthesis

Overview

Course Dates	1st-15th March, 2017
Host Institute	IIT Madras
No. of Credits	2
Maximum No. of Participants	60
You Should Attend If...	<ul style="list-style-type: none">▪ You are a student in the area of analog/mixed signal/RF/microwave IC design.▪ You wish to learn from a world renowned expert in the area.▪ You want to get up to speed on the important area of PLLs
Course Registration Fees	<p>The participation fees for taking the course is as follows: Student Participants: Rs.2000 Faculty Participants: Rs.6000 Government Research Organization Participants: Rs.20000 Industry Participants: Rs.80000</p> <p>The above fee is towards participation in the course, the course material, computer use for tutorials and assignments, and laboratory equipment usage charges.</p> <p>Mode of payment: Demand draft in favour of “Registrar, IIT Madras” payable at Chennai.The demand draft is to be sent to the Course Coordinator at the address given below.</p>
Accommodation	<p>The participants may be provided with hostel accommodation, depending on the availability, on payment basis. Request for hostel accommodation may be submitted through the link: http://hosteldine.iitm.ac.in/iitmhostel</p>

Course Faculty



Sudhakar Pamarti received his Ph.D. degrees in electrical engineering from the University of California at San Diego 2003. Prior to joining UCLA in 2005, he worked with Rambus Inc. developing high speed chip-to-chip electrical communication interfaces. Dr. Pamarti is interested in wireless and wireline communication system hardware, particularly in mixed signal circuits blocks such as data converters, frequency synthesizers, and clock synchronization and signal equalization circuits.

His group focuses on developing digital signal processing and communication theoretic techniques to improve the performance metrics of error-prone mixed signal circuitry. He emphasizes both the in-silicon verification and the theoretical analysis of such techniques. Typical research employs delta-sigma modulation for data conversion, frequency synthesis and power amplification; or, using CDMA for Gb/s chip-to-chip communication.

Course Coordinator

Name: Prof. Shanthi Pavan,
Department of Electrical
Engineering
IIT Madras,
Chennai 600036
Phone: (044)-22574437
E-mail: shanthi@ee.iitm.ac.in

URL:
<http://www.ee.iitm.ac.in/~shanthi>